1. Part No. Expression

<u>PHA0503SP1R5M</u>N

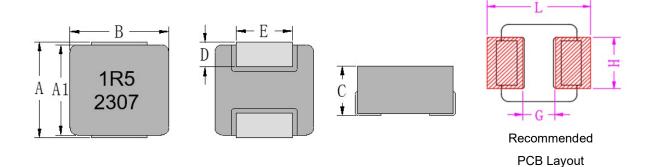
(a) (b) (c) (d) (e) (f)

(a) Series Code

(c)

- (b) Dimension Code
- (d) Inductance Code
- (e) Tolerance Code
- Material Code
- (f) Special Code

2. Configuration & Dimensions (Unit: mm)



Note: 1. The above PCB layout reference only.

2. Recommend solder paste thickness at 0.12mm and above.

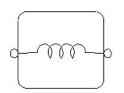
3. Marking: Top= Inductance Code, Bottom=YYWW (Year/World week)

A	A1	В	С	D	E	L	G	Н
5.6±0.3	5.1±0.3	5.2±0.2	2.8±0.2	1.3±0.3	3.0±0.3	6.5 Ref	2.2 Ref	3.5 Ref

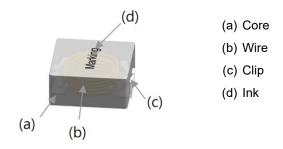
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3. Schematic



4. Material List



5. General Specifications

- (a) Operating Temp.: -40°C to +125°C (including self-temperature rise)
- (b) Storage Temp.: -40°C to +125°C (on board)
- (c) All test data referenced to 25°C ambient.
- (d) Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C.
- (e) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.
- (f) Rated Current: The lower value of Isat and Irms.
- (g) Part Temperature (Ambient + Temp. Rise): Should not exceed 125°C under worst case operating conditions.
- (h) Storage Condition (Component in its packaging)
 - i) Temperature: Less than 40°C
 - ii) Humidity: Less than 85% RH

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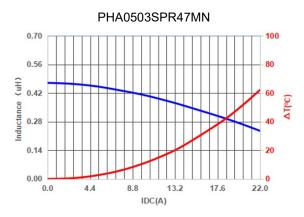
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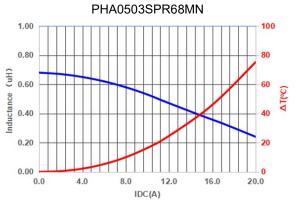
6. Electrical Characteristics

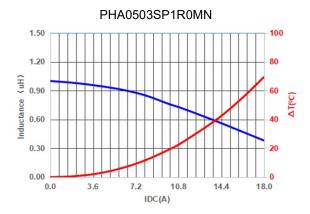
Part Number	Inductance (µH) ±20%	Irms (A)		lsat (A)		DCR (mΩ)	
		Тур	Max	Тур	Max	Тур	Max
PHA0503SPR47MN	0.47	18.0	16.0	14.0	12.5	3.1	3.7
PHA0503SPR68MN	0.68	15.0	13.0	11.5	10.5	3.7	4.3
PHA0503SP1R0MN	1.00	14.0	12.5	11.0	10.5	5.6	6.7
PHA0503SP1R5MN	1.50	13.0	12.0	10.5	9.5	8.3	10.0
PHA0503SP2R2MN	2.20	10.5	9.5	8.5	7.5	12.0	14.4
PHA0503SP3R3MN	3.30	8.5	8.0	7.5	6.5	16.0	19.2
PHA0503SP4R7MN	4.70	7.0	6.6	6.5	5.5	26.0	31.2
PHA0503SP5R6MN	5.60	6.5	5.8	6.1	5.2	33.0	39.6
PHA0503SP6R8MN	6.80	6.3	5.4	5.9	5.0	36.0	43.2
PHA0503SP8R2MN	8.20	5.8	5.1	5.5	4.7	46.0	55.2
PHA0503SP100MN	10.00	5.2	4.6	5.0	4.2	58.0	69.6
PHA0503SP150MN	15.00	4.1	3.6	2.6	2.2	90.0	108.0
PHA0503SP220MN	22.00	3.6	3.3	2.2	1.9	111.0	133.2

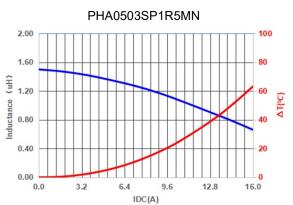
Test Frequency: 1.0V/100kHz

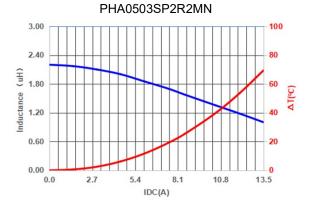
7. Characteristics Curve



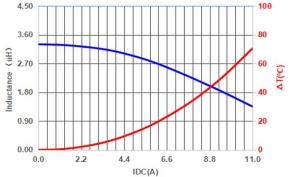






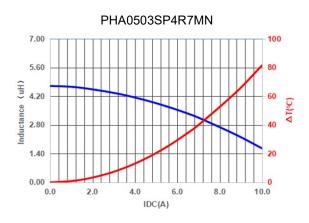


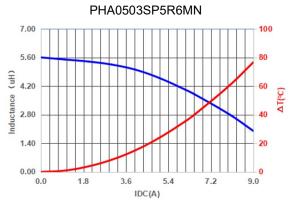
PHA0503SP3R3MN

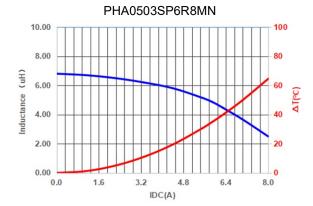


NOTE: Specifications subject to change without notice. Please check our website for latest information.

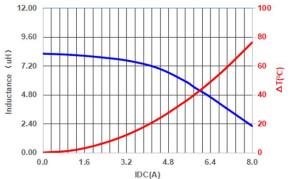
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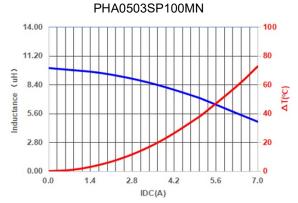


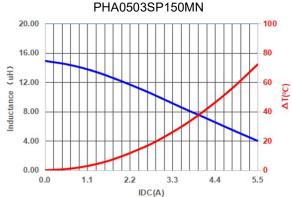




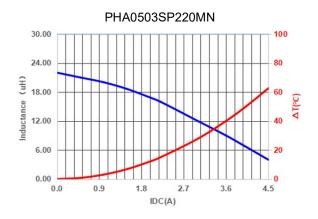
PHA0503SP8R2MN











8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

8-1. IR Soldering Reflow

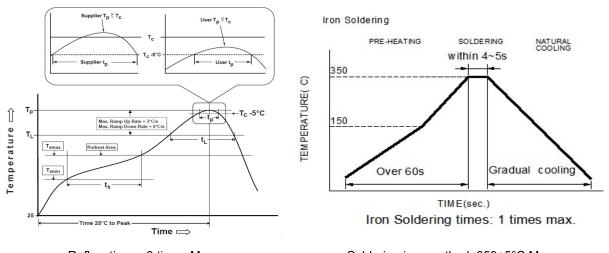
Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

8-2. Iron Reflow

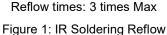
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

Note:

- (a) Preheat circuit and products to 150°C.
- (b) 355°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.



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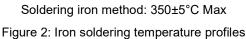


Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly	
Preheat		
-Temperature Min (T _{smin})	150°C	
-Temperature Max (T _{smax})	200°C	
-Time (t _s) from (T _{smin} to T _{smax})	60-120seconds	
Ramp-up rate (T∟to T _p)	3°C /second max.	
Liquids temperature (T _L)	217°C	
Time (t _L) maintained above T_L	60-150 seconds	
Classification temperature (T _c)	See Table (1.2)	
Time (t_p) at Tc- 5°C (Tp should be equal to or less than Tc.)	*< 30 seconds	
Ramp-down rate (T _P to T _L)	6°C /second max.	
Time 25°C to peak temperature	8 minutes max.	

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

*Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

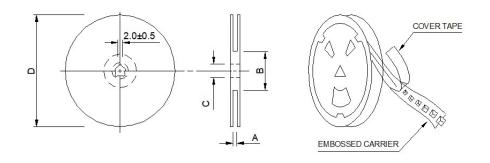
,	0		•	(3)
	Package	Volume mm ³	Volume mm ³	Volume
	Thickness	<350	350-2000	mm ³ >2000
PB-Free	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
Assembly	≥2.5mm	250°C	245°C	245°C

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

Reflow is referred to standard IPC/JEDEC J-STD-020E.

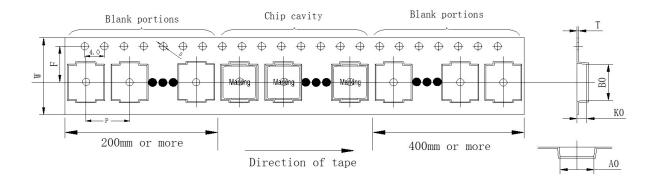
9. Packaging Information

9-1. Reel Dimension (Unit: mm)



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13" x 12mm	12.4+2.0/-0.0	100.0±2.0	13.0+0.5/-0.2	330.0 Ref

9-2. Tape Dimension (Unit: mm)



B0	B0 A0		Р	
6.50±0.10 5.70±0.10		3.30±0.10	8.00±0.10	
W	F	т	D	
12.00±0.30	5.50±0.10	0.35±0.05	1.50±0.10	

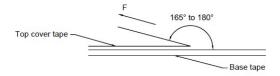
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9-3. Packaging Quantity (Unit: Pcs)

Chip/ Reel	2,000
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9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

Application Notice

1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.
- 2. Transportation
 - (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - (b) Vacuum pick up is strongly recommended for individual components.
 - (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.