

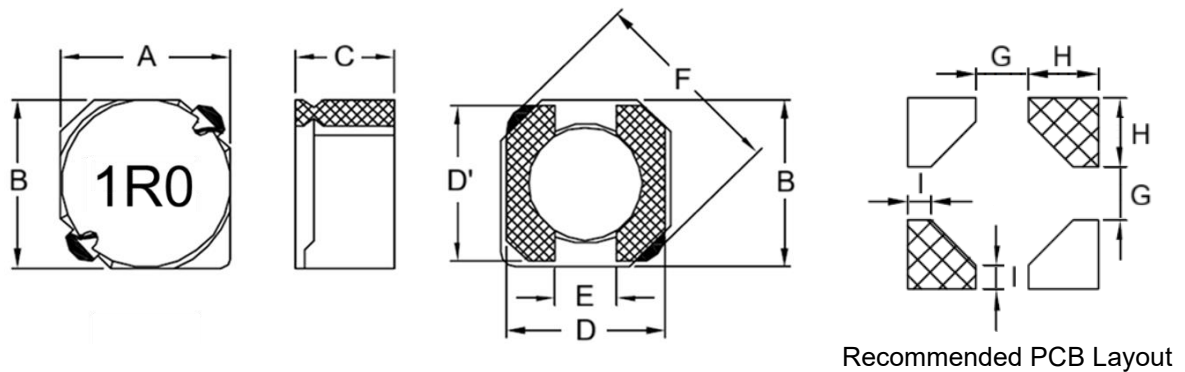
## 1. Part No. Expression

**SSC04021R0YZF**

(a) (b) (c) (d) (e) (f)

- |                     |                    |
|---------------------|--------------------|
| (a) Series Code     | (d) Tolerance Code |
| (b) Dimension Code  | (e) Special Code   |
| (c) Inductance Code | (f) Packaging Code |

## 2. Configuration & Dimensions (Unit: mm)

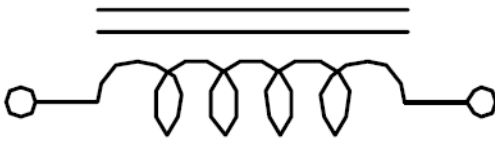


- Note:
1. The above PCB layout reference only.
  2. Marking: Inductance Code

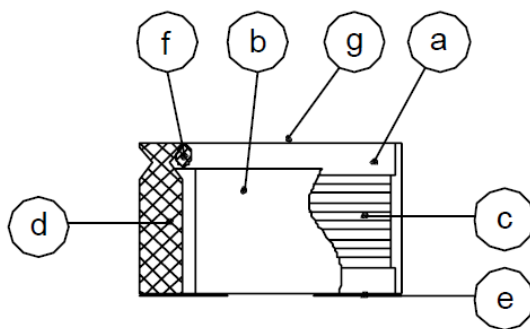
A	B	C	D	D'
4.7±0.3	4.7±0.3	2.0 Max	4.5 Ref	4.5 Ref
E	F	G	H	I
1.5 Ref	6.9 Max	1.7 Ref	1.8 Ref	0.8 Ref

NOTE: Specifications subject to change without notice. Please check our website for latest information.

### 3. Schematic



### 4. Material List



- (a) DR Core
- (b) RI Core
- (c) Wire
- (d) Terminal
- (e) Adhesive
- (f) Adhesive
- (g) Ink

### 5. General Specifications

- (a) Operating Temp.: -40°C to +125°C (including self-temperature rise)
- (b) All test data referenced to 25°C ambient.
- (c) Heat Rated Current (Irms) will cause the coil temperature rise  $\Delta T$  of 40°C Max.
- (d) Saturation Current (Isat) will cause inductance L0 to drop 35% Max.
- (e) Rated Current: The lower value of Isat and Irms.
- (f) Resistance to solder heat: 260° C, 10 secs.
- (g) Storage Condition (Component in its packaging)
  - i) Temperature: -10°C to 40°C
  - ii) Humidity: Less than 60% RH

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## 6. Electrical Characteristics

Part Number	Inductance ( $\mu$ H) @0A	Test Frequency	DCR (m $\Omega$ ) Max	IDC (A) Max
SSC04021R0YZF	1.0	0.5V/7.96MHz	45	1.72
SSC04022R2YZF	2.2	0.5V/7.96MHz	75	1.32
SSC04022R7YZF	2.7	0.5V/7.96MHz	105	1.28
SSC04023R3YZF	3.3	0.5V/7.96MHz	110	1.04
SSC04023R9YZF	3.9	0.5V/7.96MHz	155	0.88
SSC04024R7YZF	4.7	0.5V/7.96MHz	162	0.84
SSC04025R6YZF	5.6	0.5V/7.96MHz	170	0.80
SSC04026R8YZF	6.8	0.5V/7.96MHz	180	0.76
SSC04028R2YZF	8.2	0.5V/7.96MHz	190	0.68
SSC0402100YZF	10.0	0.5V/100KHz	200	0.61
SSC0402100MZF	10.0	0.5V/100KHz	200	0.61
SSC0402120YZF	12.0	0.5V/100KHz	210	0.56
SSC0402150YZF	15.0	0.5V/100KHz	240	0.50
SSC0402180YZF	18.0	0.5V/100KHz	338	0.48
SSC0402220YZF	22.0	0.5V/100KHz	397	0.41
SSC0402270YZF	27.0	0.5V/100KHz	441	0.35
SSC0402330YZF	33.0	0.5V/100KHz	694	0.32
SSC0402390YZF	39.0	0.5V/100KHz	709	0.30

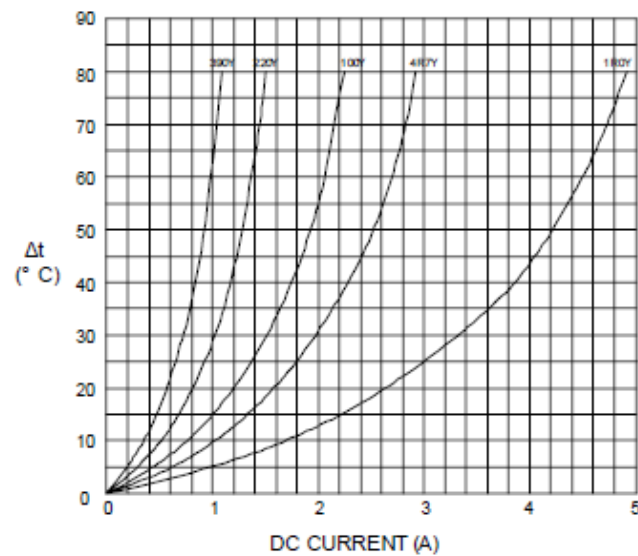
Note:

Tolerance Code: M=±20%, Y=±30%

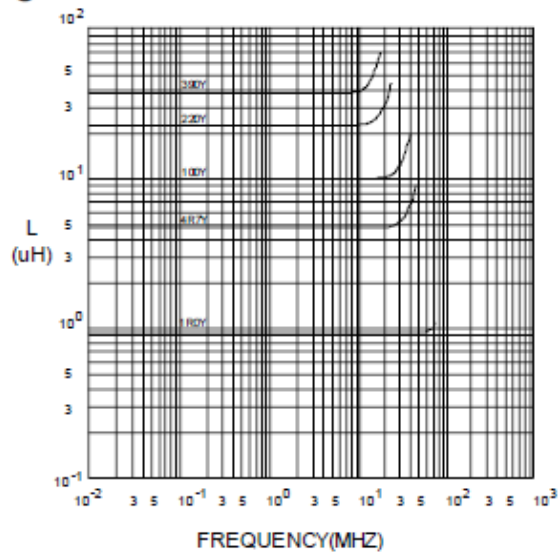
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## 7. Characteristics Curves

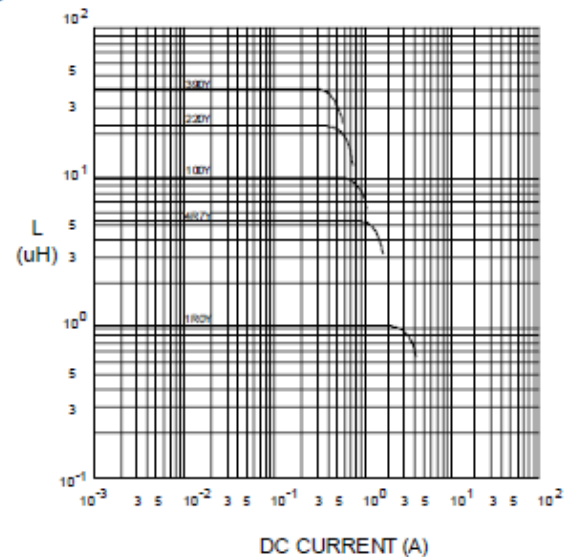
@ TEMP. RISE VS. DC SUPERPOSITION RESPONSE CURVE



@ INDUCTANCE VS. FREQUENCY RESPONSE CURVE



@ INDUCTANCE VS. DC SUPERPOSITION RESPONSE CURVE



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## 8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### 8-1. IR Soldering Reflow

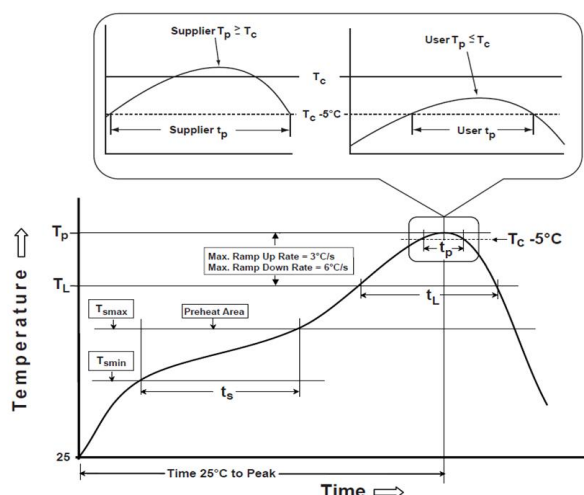
Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020E).

### 8-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

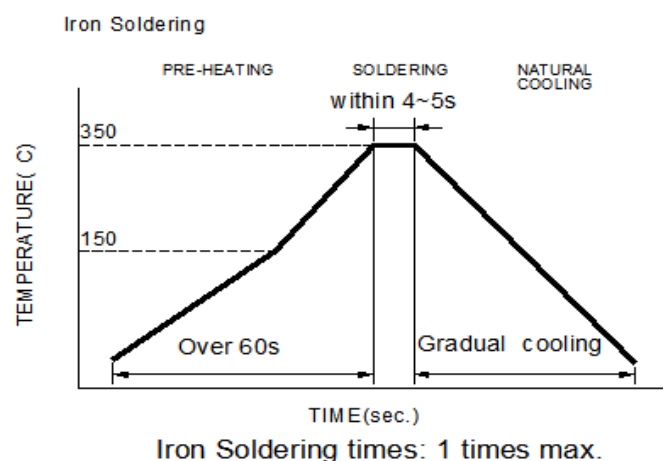
Note:

- Preheat circuit and products to 150°C.
- 355°C tip temperature (Max.)
- Never contact the ceramic with the iron tip
- 1.0mm tip diameter (Max.)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5 sec.



Reflow times: 3 times Max

Figure 1: IR Soldering Reflow



Soldering iron method: 350±5°C Max

Figure 2: Iron soldering temperature profiles

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**Table (1.1) Reflow Profiles**

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min ( $T_{\min}$ )	150°C
-Temperature Max ( $T_{\max}$ )	200°C
-Time ( $t_s$ ) from ( $T_{\min}$ to $T_{\max}$ )	60-120seconds
Ramp-up rate ( $T_L$ to $T_p$ )	3°C /second max.
Liquids temperature ( $T_L$ )	217°C
Time ( $t_L$ ) maintained above $T_L$	60-150 seconds
Classification temperature ( $T_c$ )	See Table (1.2)
Time ( $t_p$ ) at $T_c - 5^\circ\text{C}$ ( $T_p$ should be equal to or less than $T_c$ .)	* < 30 seconds
Ramp-down rate ( $T_p$ to $T_L$ )	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

**$T_p$** : maximum peak package body temperature,  **$T_c$** : the classification temperature.

For user (customer)  **$T_p$**  should be equal to or less than  **$T_c$** .

\*Tolerance for peak profile temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.

**Table (1.2) Package Thickness/Volume and Classification Temperature ( $T_c$ )**

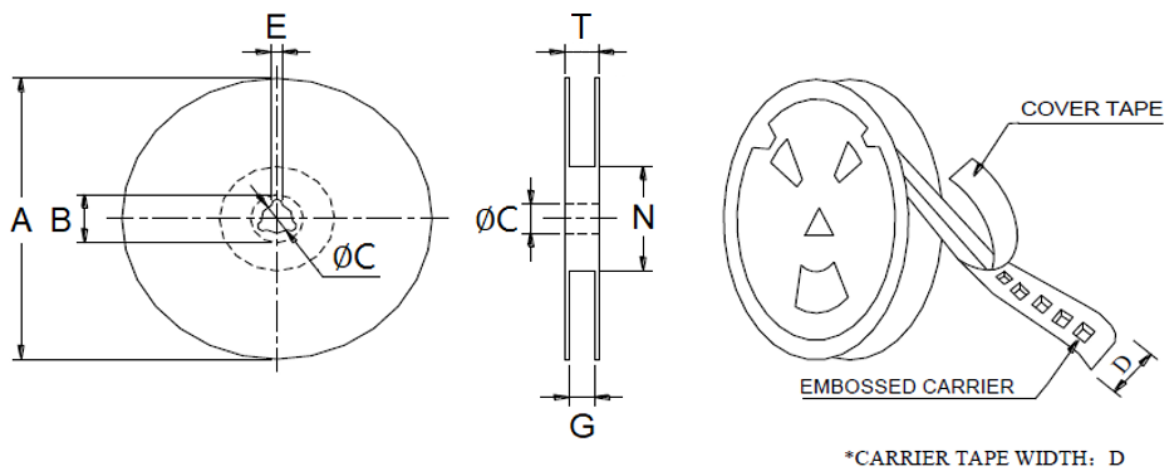
	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

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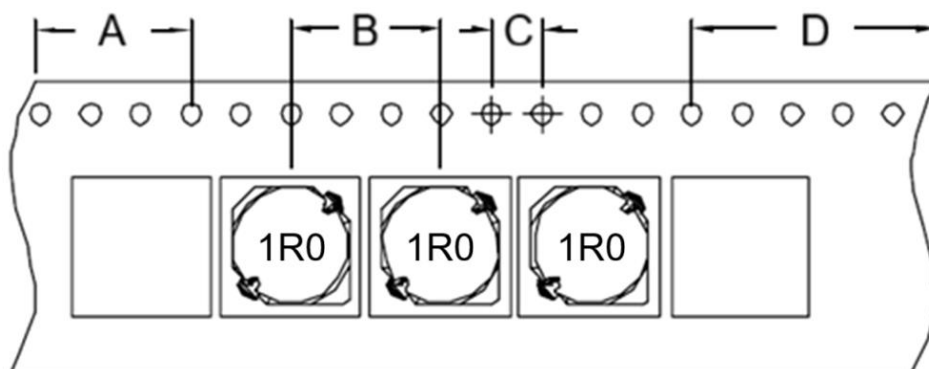
## 9. Packaging Information

### 9-1. Reel Dimension (Unit: mm)



Type	A	B	C	D	E	G	N	T
13"x16mm	330.0 Ref	21.0 Ref	13.0 Ref	16.0 Ref	2.0 Ref	18.0 Max	50.0 Min	22.4 Ref

### 9-2. Tape Dimension (Unit: mm)



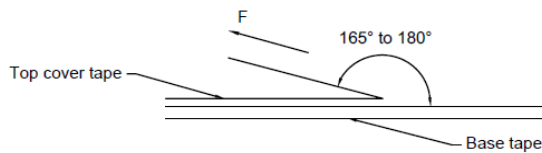
A	B	C	D
200	8	4	400

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## 9-3. Packaging Quantity (Unit: Pcs)

INNER : REEL			OUTER : CARTON		
QTY(PCS)	G.W(gw)	STYLE	QTY(PCS)	G.W.(Kg)	SIZE(cm)
3,000	2,550	13-16	18,000	18.8	38 x 36.5 x 21

## 9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

## Application Notice

### 1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Recommended products should be used within 12 months from the time of delivery.
- (b) The packaging material should be kept where no chlorine or sulfur exists in the air.

### 2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

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