

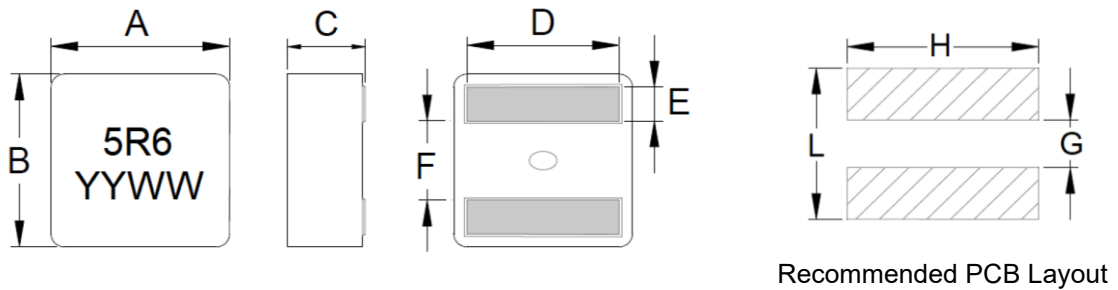
1. Part No. Expression

P I F Q 0 5 0 5 L R 5 R 6 M N

(a) (b) (c) (d) (e) (f)

- | | |
|--------------------|---------------------|
| (a) Series Code | (d) Inductance Code |
| (b) Dimension Code | (e) Tolerance Code |
| (c) Material Code | (f) Special Code |

2. Configuration & Dimensions (Unit: mm)

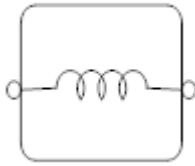


- Note:
1. The above PCB layout reference only.
 2. Recommend solder paste thickness at 0.12 mm and above.
 3. Marking: Top= Inductance Code, Bottom=YYWW (Year/World week), Black

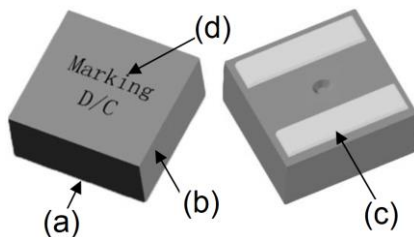
A	B	C	D	E
5.50±0.20	5.30±0.20	4.80±0.20	4.30±0.30	1.10±0.20
F	L	G	H	-
2.30±0.25	4.50 Ref	2.00 Ref	4.70 Ref	-

NOTE: Specifications subject to change without notice. Please check our website for latest information.

3. Schematic



4. Material List



- (a) Core
- (b) Wire
- (c) Solder
- (d) Ink

5. General Specifications

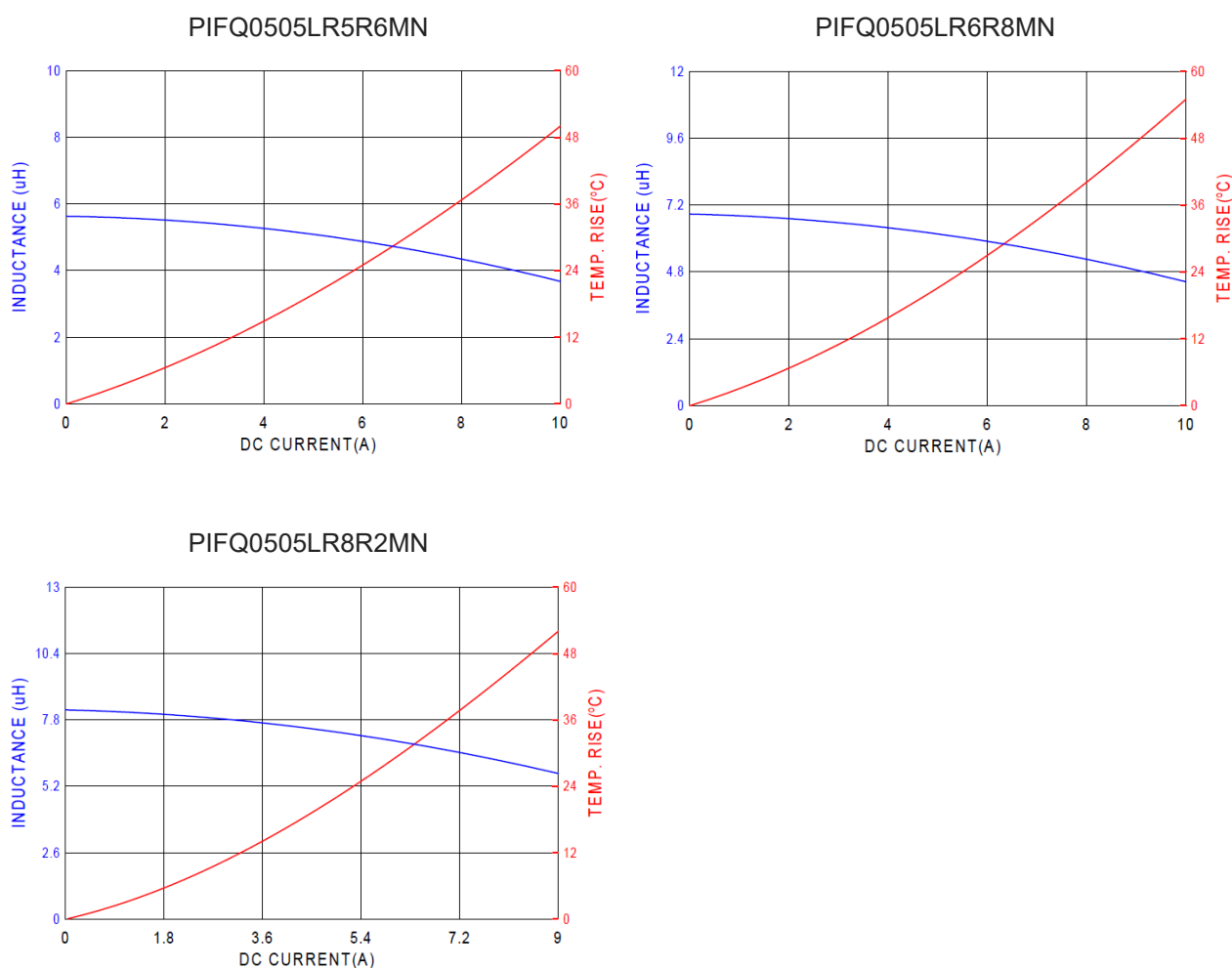
- (a) Reliability test for this part meets AEC-Q200 standard.
- (b) Operating Temp.: - 55°C to + 155°C (including self-temperature rise)
- (c) Storage Temp.: - 55°C to + 155°C (on board)
- (d) All test data referenced to 25°C ambient.
- (e) Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 20°C & 40°C.
- (f) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.
- (g) Rated DC Current: The lower value of Irms and Isat.
- (h) Part Temperature (Ambient + Temp. Rise): Should not exceed 155°C under worst case operating conditions.
- (i) Maximum Operating Voltage: 15V
- (j) Storage Condition (Component in its packaging)
 - i) Temperature: Less than 40°C
 - ii) Humidity: Less than 60% RH

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6. Electrical Characteristics

Part Number	Inductance (μ H) @0A $\pm 20\%$	Test Frequency	I _{rms} (A) Typ		I _{sat} (A)		DCR (m Ω)	
			20°C rise	40°C rise	Typ	Max	Typ	Max
PIFQ0505LR5R6MN	5.60	0.1V/100KHz	5.30	7.20	8.60	7.20	22.0	24.2
PIFQ0505LR6R8MN	6.80	0.1V/100KHz	4.80	6.40	7.80	6.60	26.0	28.6
PIFQ0505LR8R2MN	8.20	0.1V/100KHz	4.60	6.10	7.20	6.10	29.5	32.5

7. Characteristics Curve



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8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

8-1. IR Soldering Reflow

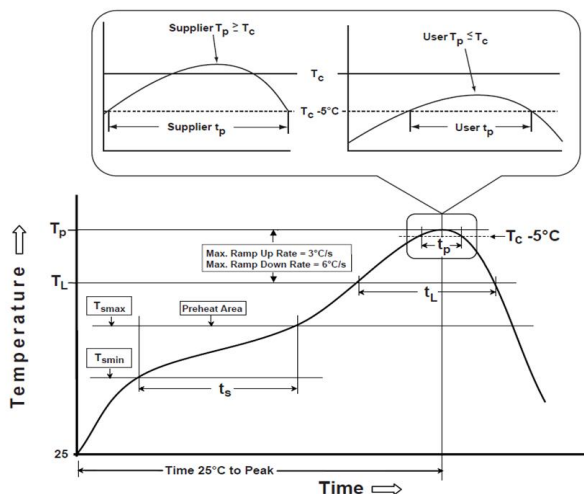
Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020F).

8-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

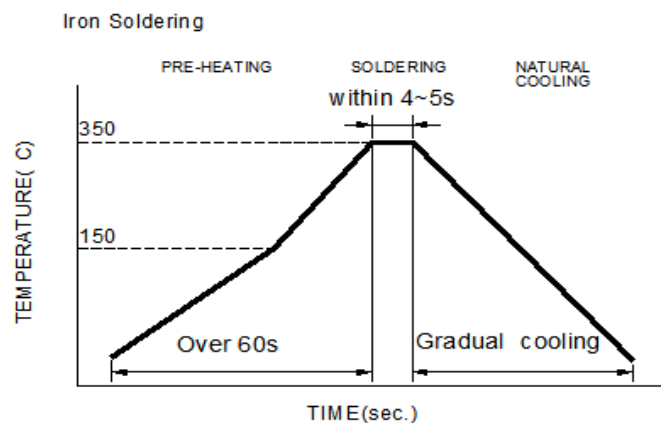
Note:

- Preheat circuit and products to 150°C.
- 350°C tip temperature (Max.)
- Never contact the ceramic with the iron tip
- 1.0mm tip diameter (Max.)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5 sec.



Reflow times: 3 times Max

Figure 1: IR Soldering Reflow



Iron Soldering times : 1 times max

Figure 2: Iron soldering temperature profiles

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Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T_{smin})	150°C
-Temperature Max (T_{smax})	200°C
-Time (t_s) from (T_{smin} to T_{smax})	60-120seconds
Ramp-up rate (T_L to T_p)	3°C /second max.
Liquids temperature (T_L)	217°C
Time (t_L) maintained above T_L	60-150 seconds
Classification temperature (T_c)	See Table (1.2)
Time (t_p) at $T_c - 5^\circ\text{C}$ (T_p should be equal to or less than T_c .)	< 30 seconds
Ramp-down rate (T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p: maximum peak package body temperature, **T_c**: the classification temperature.

For user (customer) **T_p** should be equal to or less than **T_c**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020F.

8-3. Soldering Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceeded as shown in the Figure below.

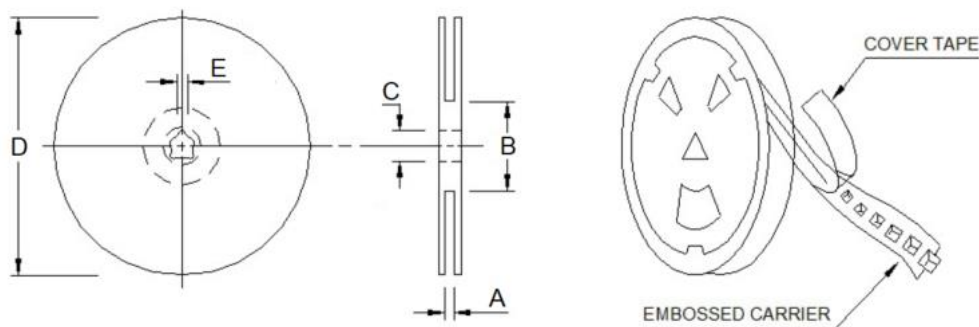
Minimum fillet height = soldering thickness + 25% product height.



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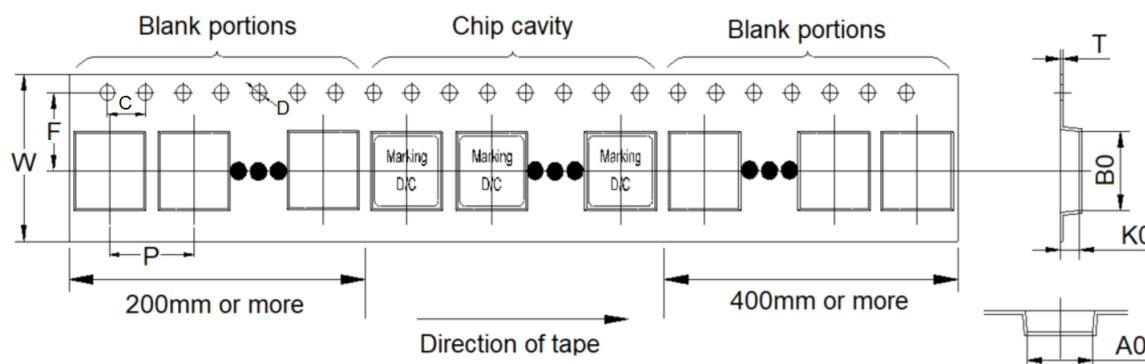
9. Packaging Information

9-1. Reel Dimension (Unit: mm)



Type	A	B	C	D	E
13"x16mm	16.4 Ref	100.0 Ref	13.0 Ref	330.0 Ref	2.0 Ref

9-2. Tape Dimension (Unit: mm)



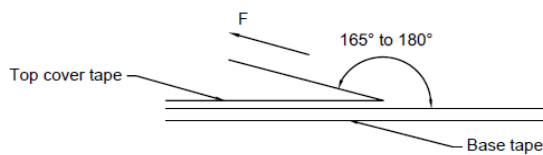
B0	A0	K0	P	W
5.70±0.10	6.00±0.10	5.30±0.10	8.00±0.10	16.00±0.30
F	T	D	C	-
7.50±0.10	0.35±0.05	1.50±0.10	4.00	-

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9-3. Packaging Quantity (Unit: Pcs)

Chip/ Reel	1,500
Inner box	3,000
Carton	12,000

9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

Application Notice

1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
- (b) Recommended products should be used within 12 months from the time of delivery.
- (c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

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