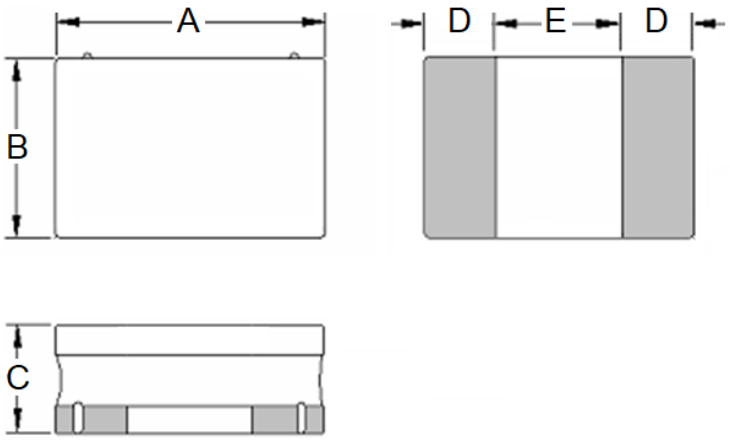
1. Part No. Expression

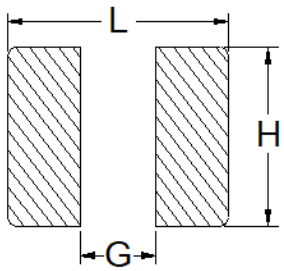
S P S 2 5 2 0 1 2 E R 2 4 M F

(a) (b) (c) (d) (e) (f)

(a)	Series Code	(d)	Inductance Code
(b)	Dimension Code	(e)	Tolerance Code
(c)	Material Code	(f)	Packaging Code

2. Configuration & Dimensions (Unit: mm)





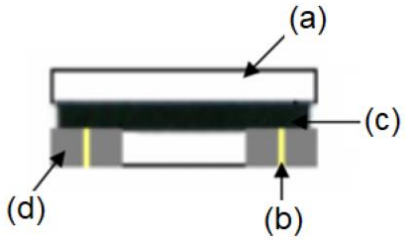
Recommended PCB Layout

Note: The above PCB layout reference only.

A	B	C	D
2.50+0.20/-0.10	2.00+0.35/-0.05	1.20 Max	0.85 Ref
E	L	G	H
0.80 Ref	2.90 Ref	0.80 Ref	2.40 Ref

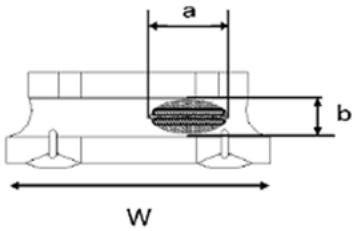
3. Schematic

4. Material List



(a) Core
(b) Wire
(c) Glue
(d) Terminal

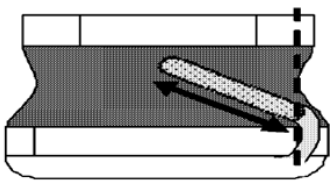
Void appearance tolerance limit & size of voids occurring to coating resin is specified below.



Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below:

1. Width direction (dimension a) : Acceptable when a ≦ w/2;
   Nonconforming when a＞w/2
2. Length direction (dimension b): Dimension b is not specified
3. The total area of exposed wire occurring to each side is not greater than 50% of coating resin area and is acceptable



External appearance criterion for exposed wire.

Exposed end of the winding wire at the secondary side should be 2mm and below.

NOTE: Specifications subject to change without notice. Please check our website for latest information.

5. General Specifications

(a) Operating Temp.: -40°C to +125°C (including self-temperature rise)

(b) Storage Temp.: -40°C to +125°C (on board)

(c) All test data referenced to 25°C ambient.

(d) Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C.

(e) Saturation Current (Isat) will cause inductance L0 to drop approximately 30%.

(f) Storage Condition (Component in its packaging)

i) Temperature: Less than 40°C

ii) Humidity: Less than 60% RH

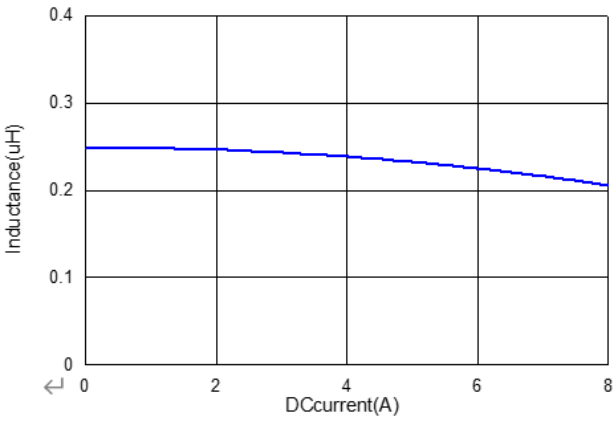
6. Electrical Characteristics

Part Number	Inductance (μH) @0A ±20%	Test Frequency	Irms (A)		Isat (A)		DCR (Ω)	
			Typ	Max	Typ	Max	Typ	Max
SPS252012ER24MF	0.24	0.1V/1MHz	4.70	4.20	8.00	6.50	0.024	0.028
SPS252012ER33MF	0.33	0.1V/1MHz	4.50	4.00	5.70	4.60	0.027	0.032
SPS252012ER47MF	0.47	0.1V/1MHz	4.40	3.90	5.50	4.50	0.027	0.032
SPS252012ER68MF	0.68	0.1V/1MHz	3.60	3.20	4.50	3.80	0.036	0.043
SPS252012E1R0MF	1.00	0.1V/1MHz	3.50	3.15	3.90	3.40	0.045	0.057
SPS252012E1R5MF	1.50	0.1V/1MHz	2.50	2.25	3.00	2.60	0.080	0.096
SPS252012E2R2MF	2.20	0.1V/1MHz	2.30	2.00	2.70	2.30	0.085	0.102
SPS252012E3R3MF	3.30	0.1V/1MHz	1.70	1.50	2.00	1.80	0.120	0.144
SPS252012E4R7MF	4.70	0.1V/1MHz	1.50	1.30	1.90	1.60	0.230	0.276

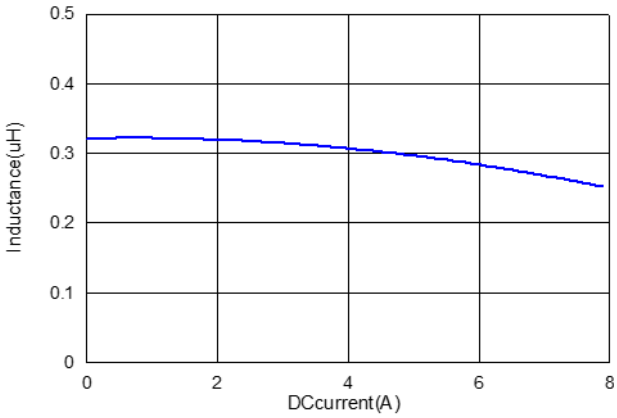
NOTE: Specifications subject to change without notice. Please check our website for latest information.

7. Characteristics Curve

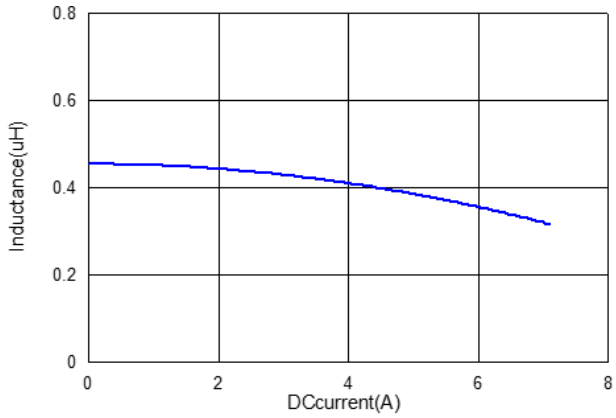
SPS252012ER24MF



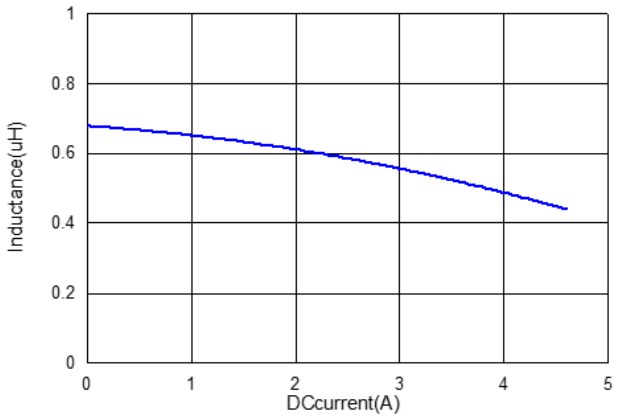
SPS252012ER33MF



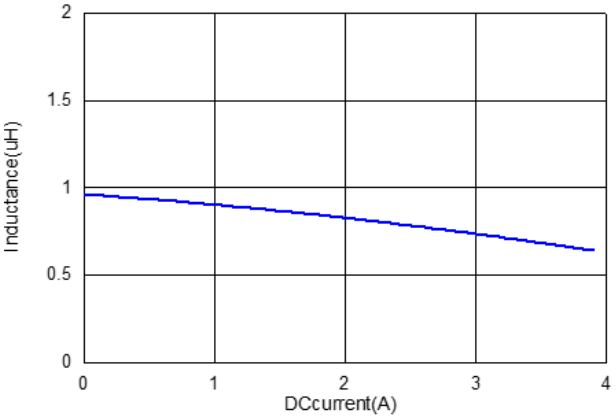
SPS252012ER47MF



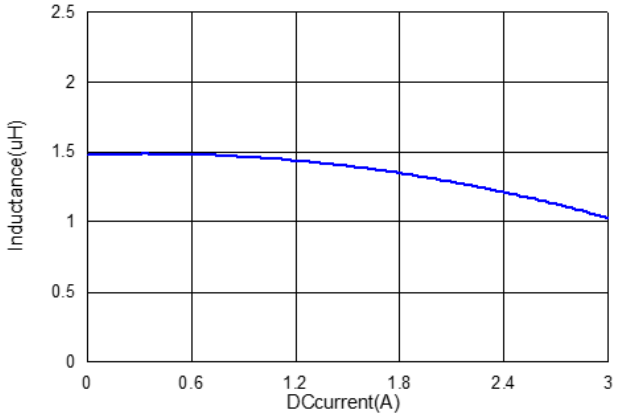
SPS252012ER68MF



SPS252012E1R0MF

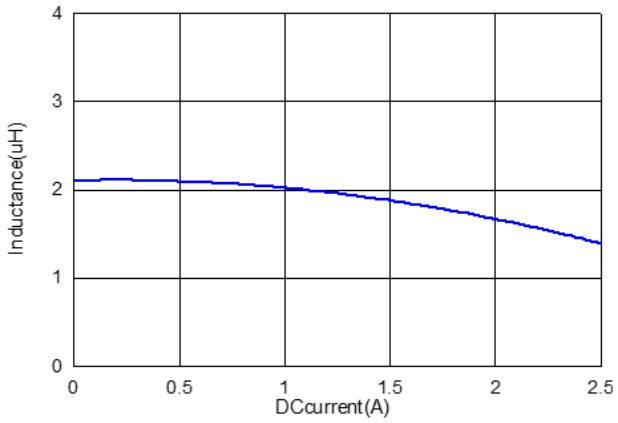


SPS252012E1R5MF

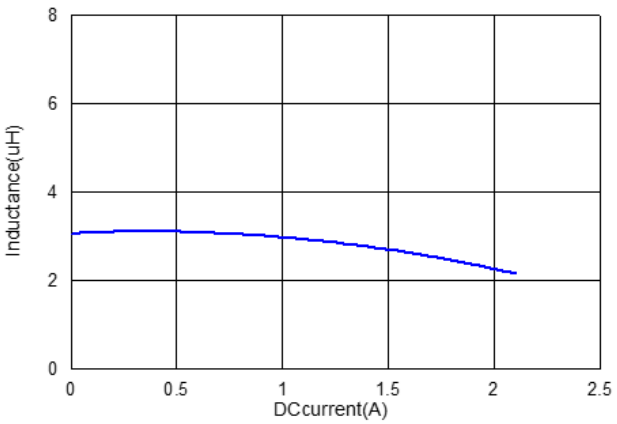


NOTE: Specifications subject to change without notice. Please check our website for latest information.

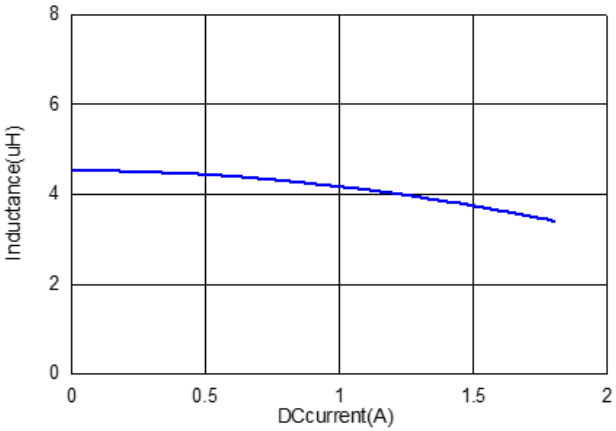
SPS252012E2R2MF



SPS252012E3R3MF



SPS252012E4R7MF



NOTE: Specifications subject to change without notice. Please check our website for latest information.

8. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

8-1. IR Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020F).

8-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

Note:

(a) Preheat circuit and products to 150°C.

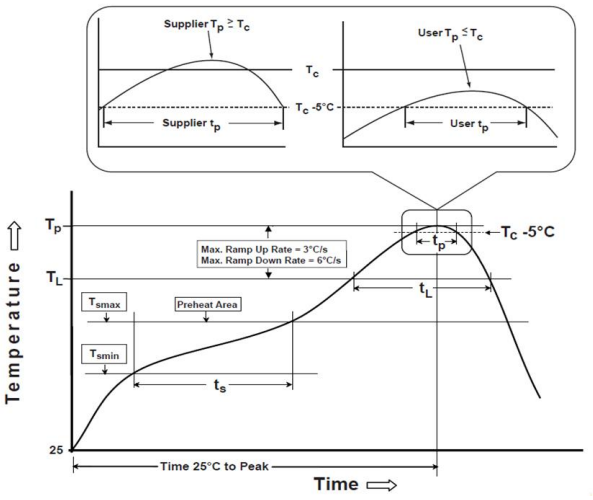
(b) 350°C tip temperature (Max.)

(c) Never contact the ceramic with the iron tip

(d) 1.0mm tip diameter (Max.)

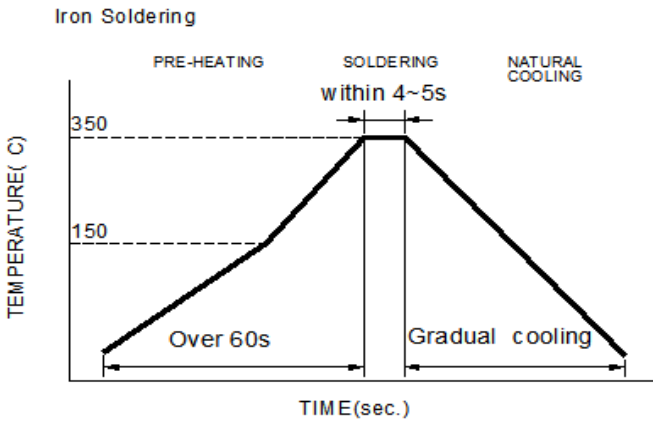
(e) Use a 20 watt soldering iron with tip diameter of 1.0mm

(f) Limit soldering time to 4~5 sec.



Reflow times: 3 times Max

Figure 1: IR Soldering Reflow



Iron Soldering times：1 times max

Figure 2: Iron soldering temperature profiles

NOTE: Specifications subject to change without notice. Please check our website for latest information.

Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min (T_{smin}) -Temperature Max (T_{smax}) -Time (t_s) from (T_{smin} to T_{smax})	 150°C 200°C 60-120seconds
Ramp-up rate ($T_L$to T_p)	3°C /second max.
Liquids temperature (T_L) Time (t_L) maintained above T_L	217°C 60-150 seconds
Classification temperature (T_c)	See Table (1.2)
Time (t_p) at Tc- 5°C (Tp should be equal to or less than Tc.)	< 30 seconds
Ramp-down rate (T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc.**

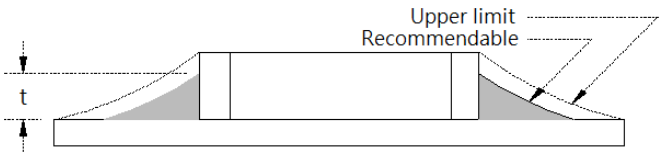
Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm^3 <350	Volume mm^3 350-2000	Volume mm^3 >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020F.

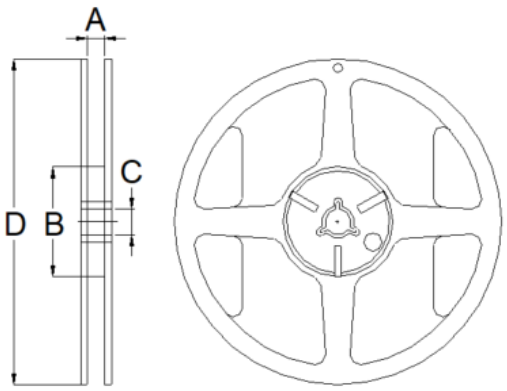
8-3. Soldering Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceeded as shown in the Figure below. Minimum fillet height = soldering thickness + 25% product height.



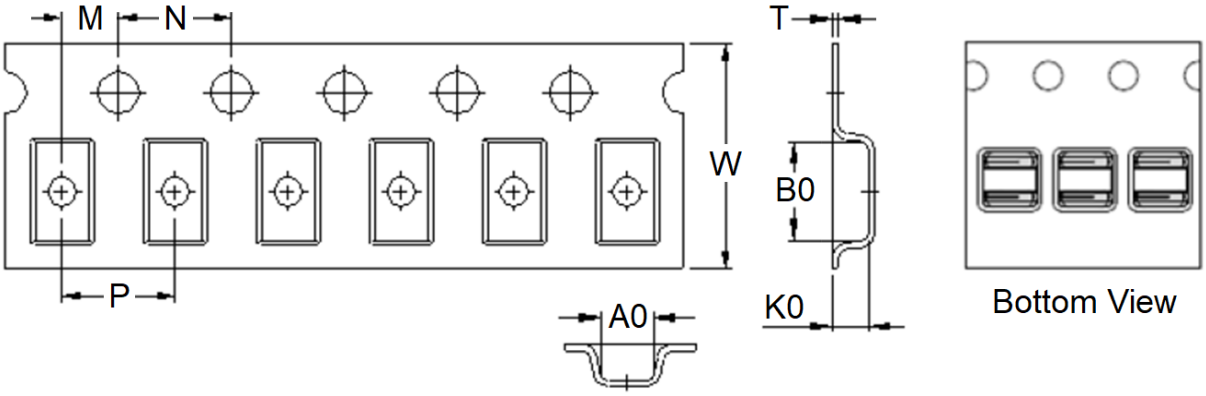
9. Packaging Information

9-1. Reel Dimension (Unit: mm)



Type	A	B	C	D
7”X8mm	8.4 Ref	50.0 Min	13.0 Ref	178.0 Ref

9-2. Tape Dimension (Unit: mm)



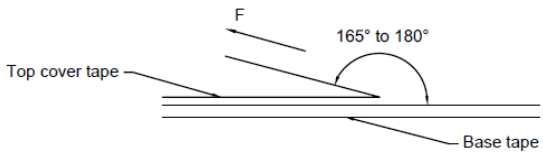
B0	A0	K0	P	T	M	N	W
3.10±0.10	2.45±0.10	1.40±0.10	4.00±0.10	0.23±0.05	2.00±0.05	4.00±0.10	8.00±0.10

NOTE: Specifications subject to change without notice. Please check our website for latest information.

9-3. Packaging Quantity (Unit: Pcs)

Chip/ Reel	2,000

9-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

Application Notice

1. Storage Conditions

To maintain the solderability of terminal electrodes:

(a) Products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.

(b) Recommended products should be used within 12 months from the time of delivery.

(c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation

(a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.

(b) Vacuum pick up is strongly recommended for individual components.

(c) Bulk handling should ensure that abrasion and mechanical shock are minimized.