

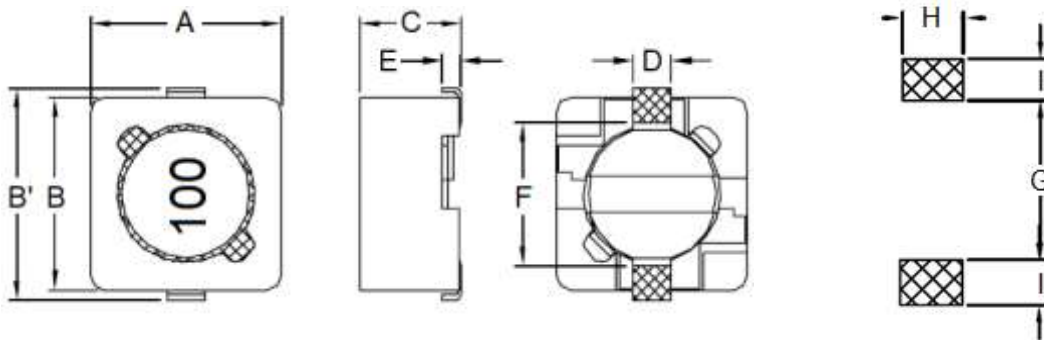
1. Part No. Expression

S D C 0 6 0 4 1 0 0 M F

(a) (b) (c) (d) (e)

- (a) Series Code
- (b) Dimension Code
- (c) Inductance Code
- (d) Tolerance Code
- (e) Packaging Code

2. Configuration & Dimensions (Unit: mm)



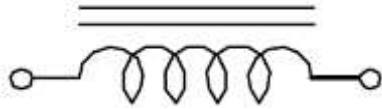
Recommended PCB Layout

- Note:
1. The above PCB layout reference only.
 2. Marking: Inductance Code

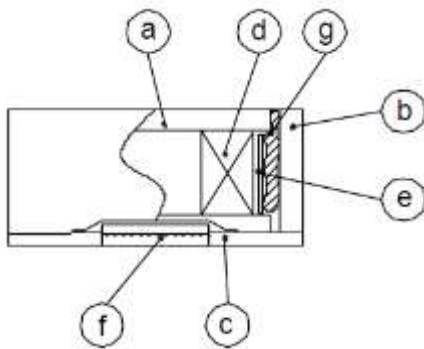
A	B	B'	C	D
6.2±0.3	5.9±0.3	6.6±0.3	5.0 Max	1.5 Ref
E	F	G	H	I
0.6 Ref	4.6 Ref	4.6 Ref	1.9 Ref	1.4 Ref

NOTE: Specifications subject to change without notice. Please check our website for latest information.

3. Schematic



4. Material List



- (a) Core
- (b) Core
- (c) Base
- (d) Wire
- (e) Tape
- (f) Terminal
- (g) Adhesive

5. General Specifications

- (a) Operating Temp.: -30°C to +105°C (including self-temperature rise)
- (b) All test data referenced to 25°C ambient.
- (c) Heat Rated Current (I_{rms}) will cause the coil temperature rise ΔT of 40°C Max.
- (d) Saturation Current (I_{sat}) will cause inductance L_0 to drop 25% Max.
- (e) Rated Current: The lower value of I_{sat} and I_{rms} .
- (f) Resistance to solder heat: 260° C, 10 secs
- (g) Storage Condition (Component in its packaging)
 - i) Temperature: -10°C to 40°C
 - ii) Humidity: Less than 60% RH

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6. Electrical Characteristics

Part Number	Inductance (uH) @0A ±20%	Test Frequency	DCR (Ω) Max	IDC (A) Max
SDC0604100MF	10	0.25V/1KHz	0.12	1.35
SDC0604120MF	12	0.25V/1KHz	0.13	1.22
SDC0604150MF	15	0.25V/1KHz	0.18	1.11
SDC0604180MF	18	0.25V/1KHz	0.24	1.02
SDC0604220MF	22	0.25V/1KHz	0.27	0.91
SDC0604270MF	27	0.25V/1KHz	0.30	0.82
SDC0604330MF	33	0.25V/1KHz	0.33	0.74
SDC0604390MF	39	0.25V/1KHz	0.37	0.69
SDC0604470MF	47	0.25V/1KHz	0.52	0.62
SDC0604560MF	56	0.25V/1KHz	0.56	0.58
SDC0604680MF	68	0.25V/1KHz	0.63	0.51
SDC0604820MF	82	0.25V/1KHz	0.71	0.46
SDC0604101MF	100	0.25V/1KHz	1.03	0.42
SDC0604121MF	120	0.25V/1KHz	1.15	0.38
SDC0604151MF	150	0.25V/1KHz	1.68	0.35
SDC0604181MF	180	0.25V/1KHz	1.87	0.32
SDC0604221MF	220	0.25V/1KHz	2.08	0.29
SDC0604271MF	270	0.25V/1KHz	2.37	0.26
SDC0604331MF	330	0.25V/1KHz	2.67	0.23
SDC0604391MF	390	0.25V/1KHz	2.94	0.22
SDC0604471MF	470	0.25V/1KHz	3.93	0.20
SDC0604561MF	560	0.25V/1KHz	5.43	0.18
SDC0604681MF	680	0.25V/1KHz	7.32	0.18
SDC0604821MF	820	0.25V/1KHz	8.24	0.15
SDC0604102MF	1000	0.25V/1KHz	9.26	0.14

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7. Soldering Specification

Mildly activated rosin fluxes are preferred. Our terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-1. IR Soldering Reflow

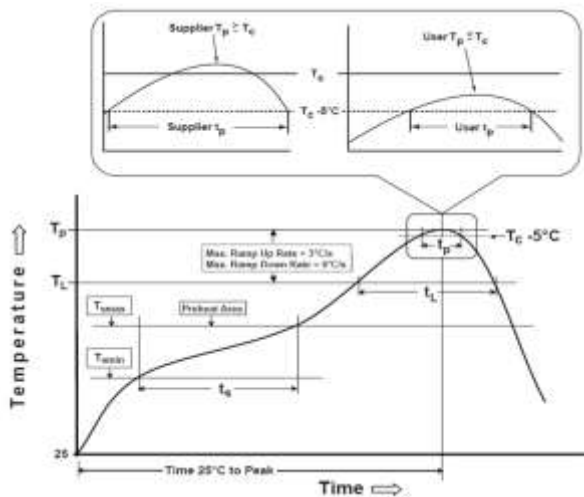
Recommended temperature profiles for lead free re-flow soldering in Figure 1, Table 1.1 & 1.2 (J-STD-020F).

7-2. Iron Reflow

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended (Figure 2).

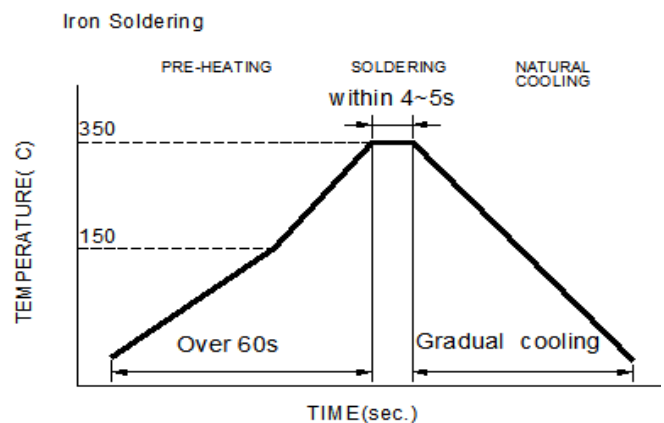
Note:

- (a) Preheat circuit and products to 150°C.
- (b) 350°C tip temperature (Max.)
- (c) Never contact the ceramic with the iron tip
- (d) 1.0mm tip diameter (Max.)
- (e) Use a 20 watt soldering iron with tip diameter of 1.0mm
- (f) Limit soldering time to 4~5 sec.



Reflow times: 3 times Max

Figure 1: IR Soldering Reflow



Iron Soldering times : 1 times max

Figure 2: Iron soldering temperature profiles

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Table (1.1) Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min (T_{smin})	150°C
-Temperature Max (T_{smax})	200°C
-Time (t_s) from (T_{smin} to T_{smax})	60-120seconds
Ramp-up rate (T_L to T_p)	3°C /second max.
Liquids temperature (T_L)	217°C
Time (t_L) maintained above T_L	60-150 seconds
Classification temperature (T_c)	See Table (1.2)
Time (t_p) at $T_c - 5^\circ\text{C}$ (T_p should be equal to or less than T_c .)	< 30 seconds
Ramp-down rate (T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p: maximum peak package body temperature, **T_c**: the classification temperature.

For user (customer) **T_p** should be equal to or less than **T_c**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020F.

7-3. Soldering Volume

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceeded as shown in the Figure below.

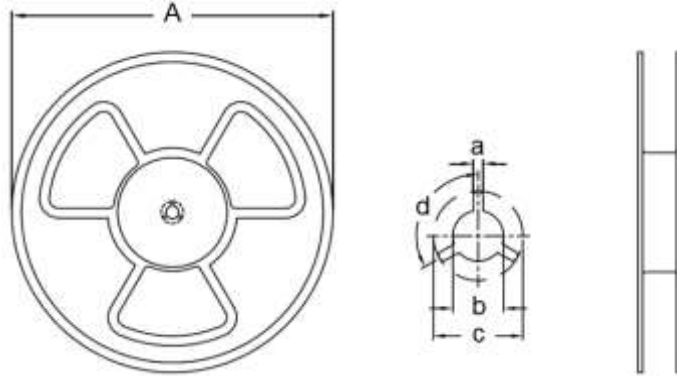
Minimum fillet height = soldering thickness + 25% product height.



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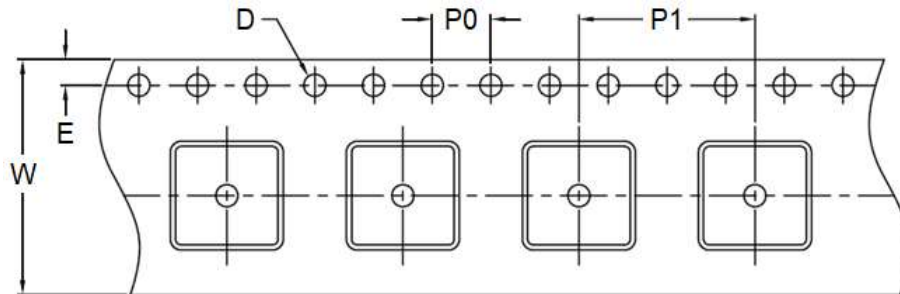
8. Packaging Information

8-1. Reel Dimension (Unit: mm)

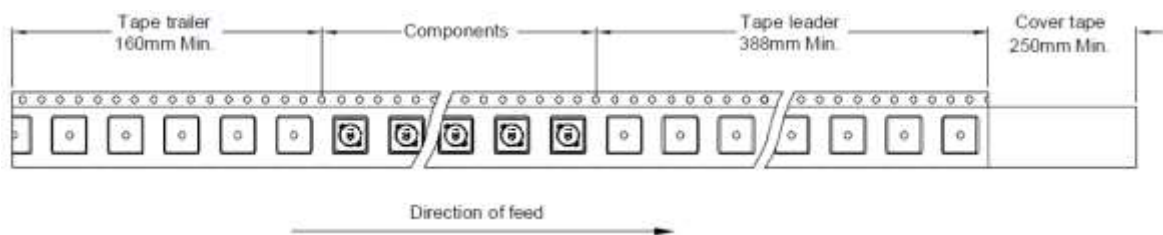


Type	A	a	b	c	d
13"x16mm	330.0 Ref	2.5 Ref	13.0 Ref	23.0 Ref	120°

8-2. Tape Dimension (Unit: mm)



W	E	D	P0	P1
16.00 Ref	1.75±0.10	1.50+0.10/-0.00	4.00±0.10	12.00 Ref

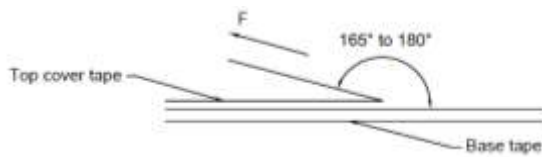


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8-3. Packaging Quantity (Unit: Pcs)

INNER : REEL		OUTER : CARTON		
QTY(PCS)	G.W(gw)	QTY(PCS)	G.W(Kg)	SIZE(cm)
1,000	0.85	16,000	18	36x36x40

8-4. Tearing Off Force



The force for tearing off cover tape is according to the follow table, in the arrow direction under the following conditions.

(Referenced ANSI/EIA-481-D-2008 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed (mm/min)
5~35	45~85	860~1060	300±10

Tape Size	8 mm	12 to 56 mm	72 mm or Wider
Tearing Off Force (grams)	10~100	10~130	10~150

Application Notice

1. Storage Conditions

To maintain the solderability of terminal electrodes:

- (a) Products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
- (b) Recommended products should be used within 12 months from the time of delivery.
- (c) The packaging material should be kept where no chlorine or sulfur exists in the air.

2. Transportation

- (a) Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- (b) Vacuum pick up is strongly recommended for individual components.
- (c) Bulk handling should ensure that abrasion and mechanical shock are minimized.

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